

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Songjie Xu
Appl. No. : 09/754,406
Filed : January 2, 2001
For : METHODOLOGY
AND APPLICATIONS
OF TIMING-DRIVEN
LOGIC
RESYNTHESIS FOR
VLSI CIRCUITS
Examiner : Thomas H. Stevens

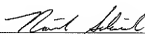
Group Art Unit: 2121

Class No.: 703-019000

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April 10, 2007

(Date)



Raimond J. Salenicks, Reg. No. 37,924

NOTIFICATION OF LOSS OF ENTITLEMENT TO
SMALL ENTITY STATUS 37 C.F.R. § 1.27(g)(2)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTENTION: Issue Branch


Dear Sir:

Applicant hereby notifies the Patent and Trademark Office that it is no longer entitled to
status as a small entity and that the claim for small entity status set forth in the written assertion
filed on January 2, 2001 is hereby withdrawn.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: April 10, 2007

By: 
Raimond J. Salenicks
Registration No. 37,924
Agent of Record
Customer No. 20,995
(619) 235-8550

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Raymond J. Salenieks, Reg. No. 37,924

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Applicant wishes to address the Examiner's statement of reasons for allowance in the Notice of Allowability mailed January 11, 2007. Applicant respectfully submits that the claims should be allowed based on the entirety of their verbiage rather than focusing on any specific portion thereof.

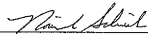
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Please charge any additional fees, including any fees for additional extension of time, or credit overpayment to Deposit Account No. 11-1410.

Respectfully submitted,

KNOBBE, MARTENS, OLSON & BEAR, LLP

Dated: April 10, 2007

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